

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,928,536 B2  
APPLICATION NO. : 09/995775  
DATED : August 9, 2005  
INVENTOR(S) : Evelyn Duesterwald et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the title page, item (54), in "Title", in column 1, lines 1-5, delete "DYNAMIC EXECUTION LAYER INTERFACE FOR REPLACING INSTRUCTIONS REQUIRING UNAVAILABLE HARDWARE FUNCTIONALITY WITH PATCH CODE AND CACHING" and insert -- SYSTEM AND METHOD FOR DYNAMICALLY PATCHING CODE --, therefor.

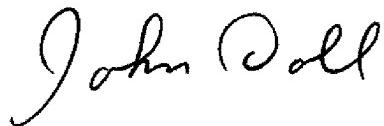
In column 1, lines 1-5, delete "DYNAMIC EXECUTION LAYER INTERFACE FOR REPLACING INSTRUCTIONS REQUIRING UNAVAILABLE HARDWARE FUNCTIONALITY WITH PATCH CODE AND CACHING" and insert -- SYSTEM AND METHOD FOR DYNAMICALLY PATCHING CODE --, therefor.

In column 15, line 7, in Claim 6, after "with" delete "a".

In column 16, line 5, in Claim 10, delete "instructions," and insert -- instructions --, therefor.

Signed and Sealed this

Fourth Day of August, 2009



JOHN DOLL  
*Acting Director of the United States Patent and Trademark Office*